Zynq Smarter Solutions – Decision Maker 1 Day Seminar

Embedded 2

EMBD-ZSS01 (v1.0)

Course Specification

Course Description

The one-day seminar will focus on some key challenges commonly encountered in developing embedded systems and how using the Zynq®-7000 All Programmable SoC (AP SoC) devices allays many of these situations.

The seminar is designed for networking, vision, and generic designers to help them identify their challenges and discover why Zynq-7000 devices are the right fit for their applications. The seminar and demos will also help students choose the appropriate path in the Zynq Smarter Solutions Workshop.

Level – Embedded 2

Course Duration –1 day

Price – $800 or 8 Xilinx Training Credits

Course Part Number – EMBD-ZSS01

Who Should Attend? – Engineers who are interested in developing embedded systems with the Xilinx Zynq All Programmable SoC processor core using the Vivado® IP integrator.

Prerequisites

- FPGA design experience
- Completion of the *Essentials of FPGA Design* course or equivalent knowledge of Xilinx Vivado software implementation tools
- Basic understanding of C programming
- Basic understanding of microprocessors
- Some HDL modeling experience

Software Tools

- Vivado Design or System Edition 2015.1

Hardware

- Architecture: Zynq-7000 All Programmable SoC and 7 series FPGAs*
- Demo board: Zynq-7000 All Programmable SoC ZedBoard*

* This course focuses on the Zynq All Programmable SoC and 7 series FPGA architectures.

** Check with Morgan Advanced Programmable Systems, Inc. for the specifics of the in-class lab board or other customizations.

After completing this comprehensive training, you will have the necessary skills to:

- Describe the hardware platform of the Zynq-7000 AP SoC device, which can handle key challenges facing Smarter Systems designers
- Use the hardware and software tool flow which eases designing Smarter Systems with Zynq-7000 AP SoC devices
- Identify the wide range of ecosystem partners providing complete software, hardware, and system solutions for Smarter Systems

Seminar Outline

- Smarter Systems Overview
- Zynq AP SoC Enables Smarter Systems
- **Demo 1:** Zynq AP SoC – Enabling Smarter Systems
- Zynq AP SoC – Hardware Overview
- Zynq AP SoC – Software Overview
- **Demo 2**: Zynq AP SoC OS Demo
- Designing with the Zynq SoC
- **Demo 3**: Designing with the Zynq AP SoC (Basic)
- Designing with the Zynq SoC: Advanced Tools Overview
- **Demo 4**: Designing with the Zynq AP SoC (Advanced)
- Zynq Development Platforms

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You must have your tuition payment information available when you enroll. We accept credit cards (Visa, MasterCard, or American Express) as well as purchase orders and Xilinx training credits.

Student Cancellation Policy

- Students cancellations received more than 7 days before the first day of class are entitled to a 100% refund. Refunds will be processed within 14 days.
- Student cancellations received less than 7 days before the first day of class are entitled to a 100% credit toward a future class.
- Student cancellations must be sent here.

MAPS Inc. Course Cancellation Policy

- We regret from time to time classes will need to be rescheduled or cancelled.
- In the event of cancellation, live on-line training may be offered as a substitute.
- MAPS may cancel a class up to 7 days before the scheduled start date of the class; all students will be entitled to a 100% refund.
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- For additional information or to schedule a private class contact us here.