

Course Description

This course is broken into a day of C language review, including variable naming, usage, and modifiers as well as an introduction to the Software Development Kit (SDK) environment, an explanation of the use of the preprocessors, program control, and proper use of functions. The second day consists of common issues and techniques employed by embedded programmers in the Xilinx SDK environment. This comprehensive course equally balances lecture modules with practical hands-on lab work.

Level – Embedded 1

Course Duration – 2 days

Price – \$1,600 or 16 Xilinx Training Credits

Course Part Number – EMBD12000-ILT

Who Should Attend? – Programmers and software engineers looking to reinforce their C skills for the embedded environment and hardware engineers interested in software engineering basics

Prerequisites

- Basic familiarity with embedded systems
- Basic background in programming

Software Tools

- Vivado® Design or System Edition 2014.1

Hardware

- Architecture: N/A*
- Demo board: Zynq®-7000 All Programmable Soc ZC702 or Zed board or Kintex®-7 FPGA KC705 board*

* This course does not focus on any particular architecture.

** Check with [Morgan Advanced Programmable Systems, Inc.](http://www.morgan-aps.com) for the specifics of the in-class lab board or other customizations.

After completing this comprehensive training, you will have the necessary skills to:

- Recognize C language symbology
- Design an effective C language program for the embedded environment
- Identify the nuances between functions and macros
- Effectively utilize numeric techniques
- Debug software using the GNU debugging tool in the SDK software environment

Course Outline

Day 1

- The C Language
- SDK Environment
- **Lab 1:** SDK Environment
- C Preprocessor
- Variables
- Control Structures
- **Lab 2:** Writing a Simple Program
- Functions and Libraries

Day 2

- Program Design
- Common Errors
- Debugging Strategies
- Dynamic Memory
- The Stack

- **Lab 3:** Debugging Stack Issues
- Numeric Techniques
- The Xilinx Embedded Environment
- **Lab 4:** Driving Xilinx Hardware

Lab Descriptions

- **Lab 1:** SDK Environment – Walks you through the process of configuring the hardware through SDK, building a simple application, and verifying that it works.
- **Lab 2:** Writing a Simple Program – Examine a piece of existing code, then complete the program using the skills developed in the previous lecture modules.
- **Lab 3:** Debugging Stack Issues – Debug stack issues, another common problem.
- **Lab 4:** Driving Xilinx Hardware – Combine the abstraction of programming with actual hardware to drive the LEDs on the demo board.

Register Today

Morgan Advanced Programmable Systems, Inc. (MAPS, Inc.) delivers public and private courses in locations throughout the central US region; including Iowa, Illinois, Kansas, Minnesota, Missouri, Nebraska, North Dakota, South Dakota and Wisconsin.

Visit morgan-aps.com/training, for full course schedule and training information.



You must have your tuition payment information available when you enroll. We accept credit cards (Visa, MasterCard, or American Express) as well as purchase orders and Xilinx training credits.

Student Cancellation Policy

- Students cancellations received more than 7 days before the first day of class are entitled to a 100% refund. Refunds will be processed within 14 days.
- Student cancellations received less than 7 days before the first day of class are entitled to a 100% credit toward a future class.
- Student cancellations must be sent [here](#).

MAPS Inc. Course Cancellation Policy

- We regret from time to time classes will need to be rescheduled or cancelled.
- In the event of cancellation, live on-line training may be offered as a substitute.
- MAPS may cancel a class up to 7 days before the scheduled start date of the class; all students will be entitled to a 100% refund.
- Under no circumstances is MAPS responsible or liable for travel, lodging or other incidental costs. Please be aware of this cancellation policy when making your arrangements.
- For additional information or to schedule a private class contact us [here](#).